

UNITED STATES DEPARTMENT OF COMMERCE Patent and Trademark Office Address: COMMISSIONER OF PATENTS AND TRADEMARKS Washington, D.C. 20231.

SERIAL NUMBER	FILING DATE	FIRST NAMED APPLICANT		ATTORNEY DOCKET NO.
0675559426	23/23/62	WIALBSEN	Ğ	

FOLE K, NILSSEN CAESAR ORIVE: RR-5 EARRINGTON: IL 60010

EXAMINER				
BEHANN				
ART UNIT	PAPER NUMBER			
8.1.2	27			

02/29/84

This is a communication from the examiner in charge of your application.

COMMISSIONER OF PATENTS AND TRADEMARKS

		'
This application has been examined	Responsive to communication filed on	This action is made final.
A shortened statutory period for response to th Failure to respond within the period for respon	nis action is set to expire 3 month(s), _ nse will cause the application to become abanc	days from the date of this letter.
Part I Notice of References Cited by Exa Notice of Art Cited by Applicant, I Information on How to Effect Draw	PTO-1449 4. Notice	ce re Patent Drawing, PTO-948. ce of informal Patent Application, Form PTO-152
Part II SUMMARY OF ACTION		
1. Claims 116 - 12	3	are pending in the application.
Of the above, claims		are withdrawn from consideration.
2. Claims		have been cancelled.
3. Claims		are allowed.
4. Claims 1/6-12	-3	are rejected.
5. Claims		are objected to.
6. Claims		are subject to restriction or election requirement.
7. This application has been filed wi	ith informal drawings which are acceptable for	examination purposes until such time as allowable subject
	een indicated, formal drawings are required in	response to this Office action.
9. The corrected or substitute drawin not acceptal as (see explanation	ngs have been received onon).	. These drawings are acceptable;
10. The proposed drawing correcting has (have) been approved by	ion and/or the proposed additional or subs the examiner. disapproved by the examine	stitute sheet(s) of drawings, filed on er (see explanation).
the Patent and Trademark Office r	no longer makes drawing changes. It is now <u>a</u> effected in accordance with the instructions se	approved. disapproved (see explanation). However, pplicant's responsibility to ensure that the drawings are at forth on the attached letter "INFORMATION ON HOW TO
12. Acknowledgment is made of the cl	laim for priority under 35 U.S.C. 119. The cer	tified copy has been received not been received
been filed in parent applicati	ion, serial no; 1	filed on
13. Since this application appears to accordance with the practice under	be in condition for allowance except for forma er Ex parte Quayle, 1935 C.D. 11; 453 O.G. 21	i matters, prosecution as to the merits is closed in 13.
14 [] Other		

The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

Claims 116 to 123 appear in the case.

Claim 119 is rejected under 35 U.S.C. 112, first paragraph, as the disclosure is enabling only for claims limited in accordance with the disclosure at pages 5-16 of the specification. See MPEP 706.03(n) and 706.03(z). The last three lines of claim 119 find no response in figure 2 of the disclosure. As to the claimed function of the last three lines in the claim, this appears to be found in capacitor 68 of figure 2. See the specification, page 9, bottom incomplete paragraph. But this connection is not as claimed; capacitor 68 is in parallel with transistor 43.

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless

- (a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.
- (b) the invention was patented or

described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 120 and 121 are rejected under 35 U.S.C.

102(a) as being clearly anticipated by Rhoads. Note
that load means T2, R4, C3 of Rhoads are connected from
center-tap A with the bottom AC input line through
connect means 36, \$1 and 34 (fig. 2A).

Claims 116, 118, and 122 are rejected under 35.
U.S.C. 102(a) as being anticipated by Rhoads.

Although it is not described as such, Phoads circuit (fig. 2A) includes a series connected inductor (primary of T2) and capacitor (C3) connected as claimed, having an <u>inherently</u> lower frequency than the fundamental frequency of the inverter output. The load and the means to connect the load "read on" resistor R4 and its connection to C3.

Claim 123 is rejected under 35 U.S.C. 102(b) as being anticipated by Grunwaldt.

The claim finds full response in figure 2 of the reference showing IC circuit 6, 7, lamp 8 connected in parallel circuit with capacitor 7, and a transformer 5 operating the inverter "at a frequency that is not higher than the natural resonant frequency of the series combination of said inductor and capacitor"

(claim 123, last three lines). Indeed, the inverter operates at the resonant frequency of LC circuit 6,7 through feedback transformer 5.

Claims 117 and 119 are rejected under 35 U.S.C.

112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which the applicant regards as the invention. The function attributed to the last three lines of each claim appears to be misdescriptive; the stated function of the capacitors is to "restrain[s] the rate of voltage change across the collector and emitter terminals of transistor 42" (claim 117) or to "restrain[s] the rate of voltage use on the collectors [of transistors 91, 92]," (claim 119; spec. pages 9 and 16). Thus the claimed and stated functions are in conflict.

The following is a quotation of 35 U.S.C. 103 which forms the basis for all obviousness rejections set forth in this Office action:

A patent may not be obtained though
the invention is not identically
disclosed or described as set forth in
section 102 of this title, if the
differences between the subject matter
sought to be patented and the prior art
are such that the subject matter as a
whole would have been obvious at the
time the invention was made to a person

having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claim 117 is rejected under 35 U.S.C. 103 as being unpatentable over Rhoads in view of the Swiss reference. Except for the claimed capacitor across the LC circuit (interpreted to be a cross a collector emitter path of a switching transistor), the claim is anticipated by Rhoads. But the Swiss reference shows this provision to be old (see Cl, C2 and the Abstract). To so provide Rhoad's inverter would have improved it.

Claim 116 is rejected under 35 U.S.C. 103 as being unpatentable over Official Notice. Official Notice is taken that a series resonant inductor and capacitor connected across the output terminals of an inverter is old. To operate the inverter frequency at less than the resonant frequency, more than the resonant frequency or in phase with the resonant frequency, in and of itself, produces no unexpected results, and to place a load, such as an oscilloscope, across the capacitor would similarly not produce patentable invention. In essence, this is all that is claimed, and it is well settled that expected results are evidence of obviousness.

As to the last paragraph of applicant's Remarks, submitted November 23, 1983, reference is made to the

parent case for an indication of allowable subject matter.

The claims in this case are drawn to more than one distinct invention or inventive concept. Compare, for example, the concepts of claims 116, 122 and 123, with the concepts of claim 117 and 119, or claims 129 and 121, or claim 118. All claims have been examined in the interest of cost effectiveness to both applicant and the Office. Should applicant unduely multiply, combine or permutate these concepts by submitting an undue number of additional claims drawn to each concept, the examiner reserves his right to make a restriction requirement in response to such amendment and force applicant to choose just one inventive concept for further prosecution.

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Figure 5 of Walden shows a half-bridge inverter using a resonant IC circuit across the center top of DC source 50, 52 and the midpoint of transistor 42, 44. Pitel shows a resonant circuit (55, 57, 59) at an inverter output.

Any inquiry concerning this communication should be directed to William H. Beha, Jr. at telephone number 703-557-5050.

Beha/lg

703-557-5050

2/27/84

William H. Belinge